

A DUAL BANDGAP VOLTAGE REFERENCE SYSTEM AND METHOD FOR  
REDUCING CURRENT CONSUMPTION DURING A STANDBY MODE OF  
OPERATION AND FOR PROVIDING REFERENCE STABILITY DURING AN  
5 ACTIVE MODE OF OPERATION

ABSTRACT OF THE DISCLOSURE

A method of operating a memory includes generating a first reference  
10 voltage and detecting an active mode of operation of the memory. Upon detection of the  
active mode, commencing the charging of a node to develop a second reference voltage  
having a desired value on the node. The word line drive voltage is generated using the first  
reference voltage while the node is charging the second reference voltage to the desired  
value. The word line drive voltage is generated using the second reference voltage once the  
15 second reference voltage on the node has been charged to the desired value. A standby  
mode of operation of the memory is detected, and upon detection of the standby mode, the  
charging of the node is terminated and the word line drive voltage is generated using the  
first reference voltage.